Call for Papers

The 13th International Symposium on Applied Reconfigurable Computing (ARC 2017)

3 - 7 April 2017, Delft, The Netherlands

Symposium Information

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via customizing, even at runtime, the topology of the underlying architecture to match the specific needs of a given application. Contemporary adaptive systems allow for the definition of architectures with functional and storage units that match in function, bit-width and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient faults in emerging device structures.

ARC aims at bringing together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology. ARC 2017 will have a series of international invited speakers that will express their view on the future of reconfigurable technology.

The ARC 2017 proceedings will be published by Springer-Verlag as a Lecture Notes in Computer Science (LNCS) series volume and will also be available through the Springer Link on-line service

Selected papers will be invited to be submitted for consideration in the special issue "Recent Progress in Applied Reconfigurable Computing" of Elsevier Journal of Microprocessors and Microsystems (MICPRO)

Submission Information

Authors are invited to submit original contributions in English including, but not limited to, the areas of interest mentioned below. Submission must be uploaded to the ARC website and identify the format of the contribution as either:

- *Long Papers: (12 pages maximum) should include mainly accomplished results (oral presentation)*

- *Short Papers: (6 pages maximum, or 8 pages max. upon payment of additional charge of 50 EUR per extra page) to be composed of work in progress or reporting recent developments (poster presentation)*

The format of the paper should be according to the Springer-Verlag LNCS Series format rules

Organizing Committee

General Chairs:
- Koen Bertels (TUDelft, The Netherlands)
- Luigi Carro (UFRGS, Brazil)

Program Chairs:
- Stephan Wong (TUDelft, The Netherlands)
- Antonio Carlos S. Beck (UFRGS, Brazil)

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- Joost Hoozemans (TUDelft, The Netherlands)

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- Pedro Diniz (USC/ISI, USA)

Publicity Chairs:
- Sorin Cotofana (TUDelft, The Netherlands)
- Pedro Diniz (USC/ISI, USA)
- Chao Wang (USTC, China)

Proceedings Chair:
- Hamid Mushtaq (TUDelft, The Netherlands)

Web Chair:
- Johan Peltenburg (TUDelft, The Netherlands)

Important Dates

NEW Submission deadline
~~11 November 2016~~ – **25 November 2016**

Author Notification
19 December 2016

Camera-Ready and Author Registration
15 January 2017

Topics of Interest

Reconfigurable Computing

Design Methods and Tools
- *High-level Languages and Compilation*
- *Simulation and Synthesis*
- *Estimation Techniques*
- *Design Space Exploration*
- *Run-Time Systems and Virtualization*

Architectures
- *Self-adaptive, evolvable*
- *Heterogeneous (GPU, APU)*
- *Low-Power Designs*
- *Approximate Computing*
- *Fine-/Coarse-/Mixed-grained*
- *Embedded MPSoCs and NoCs*
- *Resilient and Fault Tolerant*

Applications
- *Security and Cryptography*
- *Big Data and HPC*
- *Embedded Computing and DSP*
- *Robotics, Space, Bioinformatics, Automotive*
- *Safety and Mission Critical Systems*

Trends (in)
- *Teaching RC*
- *Surveys and Future Trends*
- *Benchmarks*
- *Emerging Technologies*

hosted by:

TUDelft — Delft University of Technology